L Number	Hits	Search Text	DB	Time stamp
39	67701	differential with amplifier	USPAT; EPO; JPO	2004/08/09 16:33
41	32857	impedance with match$3	USPAT; EPO; JPO	2004/08/09 13:17
42	1374501	adjust$3	USPAT; EPO; JPO	2004/08/09 12:52
43	256762	dsp or cpu	USPAT; EPO; JPO	2004/08/09 12:52
44	3909	(impedance with match$3) same adjust$3	USPAT; EPO; JPO	2004/08/09 12:52
45	58	(differential with amplifier) same ((impedance with match$3) same adjust$3)	USPAT; EPO; JPO	2004/08/09 12:52
46	6	(dsp or cpu) and ((differential with amplifier) same ((impedance with match$3) same adjust$3))	USPAT; EPO; JPO	2004/08/09 12:52
47	8	("3395358" | "3440554" | "5596610" | "5936896" | "6005438" | "6028479" | "6087899" | "6304141").PN.	USPAT	2004/08/09 13:14
48	9488	adjust$3 with impedance	USPAT; EPO; JPO	2004/08/09 15:52
49	244	(differential with amplifier) same (adjust$3 with impedance)	USPAT; EPO; JPO	2004/08/09 13:54
50	14	(dsp or cpu) and ((differential with amplifier) same (adjust$3 with impedance))	USPAT; EPO; JPO	2004/08/09 13:44
51	6	("4490634" | "4701694" | "4897555" | "5530399" | "5815103" | "6157332").PN.	USPAT	2004/08/09 13:18
52	9441	active with terminat$5	USPAT; EPO; JPO	2004/08/09 13:45
53	12037	back with match$3	USPAT; EPO; JPO	2004/08/09 13:45
54	9	(active with terminat$5) same (back with match$3)	USPAT; EPO; JPO	2004/08/09 13:45
55	2	(differential with amplifier) and ((active with terminat$5) same (back with match$3))	USPAT; EPO; JPO	2004/08/09 13:50
57	2	(active with terminat$5) same ((differential with amplifier) same (adjust$3 with impedance))	USPAT; EPO; JPO	2004/08/09 13:51
58	1	((differential with amplifier) same (adjust$3 with impedance)) and snr	USPAT; EPO; JPO	2004/08/09 13:51
59	347934	capacitor	USPAT; EPO; JPO	2004/08/09 13:54
60	38	((differential with amplifier) same (adjust$3 with impedance)) same capacitor	USPAT; EPO; JPO	2004/08/09 13:56
61	137870	"a/d" or "d/a"	USPAT; EPO; JPO	2004/08/09 13:56
62	34	((differential with amplifier) same (adjust$3 with impedance)) and ("a/d" or "d/a")	USPAT; EPO; JPO	2004/08/09 14:11
63	8286	plurality with impedance	USPAT; EPO; JPO	2004/08/09 14:11
64	1361933	switch$3	USPAT; EPO; JPO	2004/08/09 14:11
65	1536	(plurality with impedance) same switch$3	USPAT; EPO; JPO	2004/08/09 14:12
66	32	(differential with amplifier) same ((plurality with impedance) same switch$3)	USPAT; EPO; JPO	2004/08/09 14:12
67	22	adjust$3 and ((differential with amplifier) same ((plurality with impedance) same switch$3))	USPAT; EPO; JPO	2004/08/09 14:25
68	1	6580286.pn.	USPAT; EPO; JPO	2004/08/09 14:28
69	0	adjust$3 and 6580286.pn.	USPAT; EPO; JPO	2004/08/09 14:28
70	51107	input$3 with impedance	USPAT; EPO; JPO	2004/08/09 15:30
71	3862	(input$3 with impedance) same (differential with amplifier)	USPAT; EPO; JPO	2004/08/09 15:16

72	108	(adjust$3 with impedance) same ((input$3 with impedance) same (differential with amplifier))	USPAT; EPO; JPO	2004/08/09 15:16
73	1879	adjust$3 with input with impedance	USPAT; EPO; JPO	2004/08/09 15:31
75	0	(dsp or cpu) same ((differential with amplifier) same (adjust$3 with input with impedance))	USPAT; EPO; JPO	2004/08/09 15:30
74	88	(differential with amplifier) same (adjust$3 with input with impedance)	USPAT; EPO; JPO	2004/08/09 15:30
76	246	adjust$3 adj2 input adj2 impedance	USPAT; EPO; JPO	2004/08/09 15:31
77	39	(differential with amplifier) and (adjust$3 adj2 input adj2 impedance)	USPAT; EPO; JPO	2004/08/09 15:32
78	9836	(adjust$3 or contro$5) with (input with impedance)	USPAT; EPO; JPO	2004/08/09 15:52
79	9946	(adjust$3 or control$5) with (input with impedance)	USPAT; EPO; JPO	2004/08/09 15:52
80	560	((adjust$3 or control$5) with (input with impedance)) same (differential with amplifier)	USPAT; EPO; JPO	2004/08/09 15:53
81	56966	differential adj2 amplifier	USPAT; EPO; JPO	2004/08/09 15:53
82	434	((adjust$3 or control$5) with (input with impedance)) same (differential adj2 amplifier)	USPAT; EPO; JPO	2004/08/09 15:54
83	89	(((adjust$3 or control$5) with (input with impedance)) same (differential adj2 amplifier)) same capacitor	USPAT; EPO; JPO	2004/08/09 15:55
84	23650	tip with ring	USPAT; EPO; JPO	2004/08/09 15:55
85	1	(((((adjust$3 or control$5) with (input with impedance)) same (differential adj2 amplifier)) same capacitor) same (tip with ring)	USPAT; EPO; JPO	2004/08/09 15:55
86	47	(differential with amplifier) same (back with match$3)	USPAT; EPO; JPO	2004/08/09 16:33
87	4	(adjust$3 with input with impedance) and ((differential with amplifier) same (back with match$3))	USPAT; EPO; JPO	2004/08/09 16:39
88	1720	(adjust$3 or chang$3 or control$5) near2 input near2 impedance	USPAT; EPO; JPO	2004/08/09 16:40
89	64	(differential with amplifier) same ((adjust$3 or chang$3 or control$5) near2 input near2 impedance)	USPAT; EPO; JPO	2004/08/09 16:42
90	3	(tip with ring) and ((differential with amplifier) same ((adjust$3 or chang$3 or control$5) near2 input near2 impedance))	USPAT; EPO; JPO	2004/08/09 16:42
91	51918	switch$3 same (dsp or cpu)	USPAT; EPO; JPO	2004/08/09 16:43
93	1	((differential with amplifier) same ((adjust$3 or chang$3 or control$5) near2 input near2 impedance)) and (dsp or cpu)	USPAT; EPO; JPO	2004/08/09 16:43